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	LIST OF PATENTS AND PUBLICATIONS FOR	SC11244ZC	
	APPLICANT'S INFORMATION DISCLOSURE	Cynthia L. Recker et al.	
	STATEMENT	FILING DATE	GROUP
	(Use Several Sheets if Necessary)	September 1, 2000	

## REFERENCE DESIGNATION

## U.S. PATENT DOCUMENTS

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## FOREIGN PATENT DOCUMENTS

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	AO												
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## OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

MRH	AR	P. Drennan, "Integrated Circuit Device Mismatch Modeling and Characterization for Analog Circuit Design" - Ph.D. dissertation, Arizona State University, May 1999, 227 pages.
MRH	AS	P. Drennan et al., "A Comprehensive MOSFET Mismatch Model", IEEE ICMS, March 1999.
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EXAMINER

DATE CONSIDERED

Feb 19,2004

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.